

**AFFILIATED INSTITUTIONS
ANNA UNIVERSITY, CHENNAI
REGULATIONS - 2013**

M.E. ELECTRICAL DRIVES AND EMBEDDED CONTROL

I TO IV SEMESTERS (FULL TIME) CURRICULUM AND SYLLABUS

SEMESTER I

SL. NO	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1	MA7163	Applied Mathematics for Electrical Engineers	3	1	0	4
2	EB7101	Analysis of Converters and Inverters	3	1	0	4
3	EB7102	Modeling and Analysis of DC Drives	3	0	0	3
4	ET7104	Design of Embedded Systems	3	0	0	3
5	CL7103	System Theory	3	0	0	3
6		Elective I	3	0	0	3
TOTAL			18	2	0	20

SEMESTER II

SL NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1	ET7201	VLSI Architecture and Design Methodologies	3	0	0	3
2	EB7202	Control of Electric Drives	3	0	0	3
3	EB7203	Modeling and Analysis of AC Drives	3	0	0	3
4		Elective II	3	0	0	3
5		Elective III	3	0	0	3
PRACTICAL						
6	EB7211	Electric Drives Laboratory	0	0	3	2
7	EB7212	Digital Simulation of Power Electronic Circuits Laboratory	0	0	3	2
TOTAL			15	0	6	19

SEMESTER III

SL NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1		Elective IV	3	0	0	3
2		Elective V	3	0	0	3
3		Elective VI	3	0	0	3
PRACTICAL						
4	EB7311	Project Work (Phase I)	0	0	12	6
TOTAL			9	0	12	15

SEMESTER IV

SL NO.	COURSE CODE	COURSE TITLE	L	T	P	C
PRACTICAL						
1	EB7411	Project Work (Phase II)	0	0	24	12
TOTAL			0	0	24	12

TOTAL NUMBER OF CREDITS = 66

ELECTIVES FOR ELECTRICAL DRIVES AND EMBEDDED CONTROL

ELECTIVE I

SL NO.	COURSE CODE	COURSE TITLE	L	T	P	C
1	ET7001	Digital Instrumentation	3	0	0	3
2	ET7102	Microcontroller Based System Design	3	0	0	3
3	ET7103	Real Time Systems	3	0	0	3

ELECTIVE II & III

SL NO.	COURSE CODE	COURSE TITLE	L	T	P	C
1	ET7004	Programming with VHDL	3	0	0	3
2	PX7204	Power Quality	3	0	0	3
3	ET7006	Advanced Digital Signal Processing	3	0	0	3
4	ET7003	Design of Embedded Control Systems	3	0	0	3
5	ET7204	Software for Embedded Systems	3	0	0	3
6	PX7203	Special Electrical Machines	3	0	0	3

ELECTIVE IV, V & VI

SL NO.	COURSE CODE	COURSE TITLE	L	T	P	C
1	CL7005	Optimal Control and Filtering	3	0	0	3
2	ET7014	Application of MEMS Technology	3	0	0	3
3	PX7301	Power Electronics for Renewable Energy Systems	3	0	0	3
4	CL7004	Robotics and control	3	0	0	3
5	CL7007	System Identification and Adaptive Control	3	0	0	3
6	PS7004	Solar and Energy Storage Systems	3	0	0	3
7	ET7012	Computer in Networking and Digital Control	3	0	0	3
8	PS7007	Wind Energy Conversion Systems	3	0	0	3
9	CL7002	Robust Control	3	0	0	3

OBJECTIVES:

- To develop the ability to apply the concepts of Matrix theory and Linear programming in Electrical Engineering problems.
- To achieve an understanding of the basic concepts of one dimensional random variables and apply in electrical engineering problems.
- To familiarize the students in calculus of variations and solve problems using Fourier transforms associated with engineering applications.

UNIT I MATRIX THEORY (9+3)

The Cholesky decomposition - Generalized Eigen vectors, Canonical basis - QR factorization - Least squares method - Singular value decomposition.

UNIT II CALCULUS OF VARIATIONS (9+3)

Concept of variation and its properties – Euler’s equation – Functional dependant on first and higher order derivatives – Functionals dependant on functions of several independent variables – Variational problems with moving boundaries – problems with constraints - Direct methods: Ritz and Kantorovich methods.

UNIT III ONE DIMENSIONAL RANDOM VARIABLES (9+3)

Random variables - Probability function – moments – moment generating functions and their properties – Binomial, Poisson, Geometric, Uniform, Exponential, Gamma and Normal distributions – Function of a Random Variable.

UNIT IV LINEAR PROGRAMMING (9+3)

Formulation – Graphical solution – Simplex method – Two phase method - Transportation and Assignment Models

UNIT V FOURIER SERIES (9+3)

Fourier Trigonometric series: Periodic function as power signals – Convergence of series – Even and odd function: cosine and sine series – Non-periodic function: Extension to other intervals - Power signals: Exponential Fourier series – Parseval’s theorem and power spectrum – Eigen value problems and orthogonal functions – Regular Sturm-Liouville systems – Generalized Fourier series.

L:45 +T: 15 TOTAL: 60 PERIODS**REFERENCES**

1. Richard Bronson, “Matrix Operation”, Schaum’s outline series, 2nd Edition, McGraw Hill, 2011.
2. Gupta, A.S., Calculus of Variations with Applications, Prentice Hall of India Pvt. Ltd., New Delhi, 1997.
3. Oliver C. Ibe, “Fundamentals of Applied Probability and Random Processes, Academic Press, (An imprint of Elsevier), 2010.
4. Taha, H.A., “Operations Research, An introduction”, 10th edition, Pearson education, New Delhi, 2010.
5. Andrews L.C. and Phillips R.L., Mathematical Techniques for Engineers and Scientists, Prentice Hall of India Pvt.Ltd., New Delhi, 2005.

6. Elsgolts, L., Differential Equations and the Calculus of Variations, MIR Publishers, Moscow, 1973.
7. Grewal, B.S., Higher Engineering Mathematics, 42nd edition, Khanna Publishers, 2012.
8. O'Neil, P.V., Advanced Engineering Mathematics, Thomson Asia Pvt. Ltd., Singapore, 2003.
9. Johnson R. A. and Gupta C. B., "Miller & Freund's Probability and Statistics for Engineers", Pearson Education, Asia, 7th Edition, 2007.

OBJECTIVES

- To educate on the operating principle of AC/DC converters
- TO introduce three phase ac/dc converters
- To educate on DC/DC converters
- To introduce DC/AC inverters
- To educate on AC/AC converters

UNIT I SINGLE PHASE AC-DC CONVERTERS 9

Uncontrolled, half controlled and fully controlled with R-L, R-L-E loads and free wheeling diode - continuous and discontinuous modes of operation – inverter operation –Dual converter – Sequence control of converters – Performance parameters: harmonics, ripple, distortion, power factor – effect of source impedance and overlap.

UNIT II THREE PHASE AC-DC CONVERTERS 9

Uncontrolled, half controlled and fully controlled with R-L, R-L-E loads and free wheeling diodes – Inverter operation and its limit – Dual converter – Performance parameter effect of source impedance and overlap.

UNIT III DC – DC CONVERTERS 9

Principles of step-down and step-up converters – Analysis of buck, boost, buck-boost and Cuk converters – Time ratio and current limit control – Full bridge converter – Resonant and Quasi-resonant converters.

UNIT IV DC – AC CONVERTERS 9

Voltage source inverters - Principle of operation of half and full bridge inverters – 180 degree and 120 degree conduction mode inverters – Voltage control of three phase inverters using various PWM techniques – Harmonics and various harmonic elimination techniques – Analysis with R-L, R-L-E loads – Multi level inverters.

UNIT V AC – AC CONVERTERS 9

Principle of operation of -AC Voltage Controllers, Cyclo-converters – Analysis with R-L, R-L-E loads – Introduction to Matrix converters.

L:45 +T: 15 TOTAL: 60 PERIODS**REFERENCES**

1. Ned Mohan , Undeland and Robbin, "Power Electronics: Converters, Application and Design" A John Wiley and Sons, Inc., Newyork, 1995
2. Rashid M.H . "Power Electronics Circuits, Devices and Applications", Prentice Hall of India, New Delhi, 1995
3. P.C Sen ."Modern Power Electronics" Wheeler publishing Co ,First Edition ,New Delhi- 1998.

4. P.S.Bimbra , “Power Electronics”, Khanna Publishers, Eleventh Edition , 2003.
5. Bin Wu, “High Power Converters and AC Drives”, IEEE Press, A John Wiley and Sons, Inc., 2006.

EB7102

MODELLING AND ANALYSIS OF DC DRIVES

LT P C

3 0 0 3

OBJECTIVES:

- To provide knowledge about the fundamentals of magnetic circuits, energy, force and torque of multi-excited systems.
- To analyze the steady state and dynamic state operation of DC machine through mathematical modeling and simulation in digital computer.
- To provide the knowledge of theory of transformation of three phase variables to two phase variables.
- To analyze the steady state and dynamic state operation of three-phase induction machines using transformation theory based mathematical modeling and digital computer simulation.
- To analyze the steady state and dynamic state operation of three-phase synchronous machines using transformation theory based mathematical modeling and digital computer simulation.

UNIT I PRINCIPLES OF ELECTROMAGNETIC ENERGY CONVERSION

9

Magnetic circuits, permanent magnet, stored magnetic energy, co-energy - force and torque in singly and doubly excited systems – machine windings and air gap mmf - winding inductances and voltage equations.

UNIT II DC MACHINES

9

Elementary DC machine and analysis of steady state operation - Voltage and torque equations – dynamic characteristics of permanent magnet and shunt d.c. motors – Time domain block diagrams - solution of dynamic characteristic by Laplace transformation – digital computer simulation of permanent magnet and shunt d.c. machines.

UNIT III REFERENCE FRAME THEORY

9

Historical background – phase transformation and commutator transformation – transformation of variables from stationary to arbitrary reference frame - variables observed from several frames of reference.

UNIT IV INDUCTION MACHINES

9

Three phase induction machine, equivalent circuit and analysis of steady state operation – free acceleration characteristics – voltage and torque equations in machine variables and arbitrary reference frame variables – analysis of dynamic performance for load torque variations – digital computer simulation.

UNIT V SYNCHRONOUS MACHINES

9

Three phase synchronous machine and analysis of steady state operation - voltage and torque equations in machine variables and rotor reference frame variables (Park's

equations) – analysis of dynamic performance for load torque variations – digital computer simulation.

TOTAL : 45 PERIODS

REFERENCES

1. Paul C.Krause, Oleg Wasyzczyk, Scott S, Sudhoff, “Analysis of Electric Machinery and Drive Systems”, John Wiley, Second Edition, 2010.
2. P S Bimbhra, “Generalized Theory of Electrical Machines”, Khanna Publishers, 2008.
3. A.E, Fitzgerald, Charles Kingsley, Jr, and Stephan D, Umanx, “ Electric Machinery”, Tata McGraw Hill, 5th Edition, 1992
4. R. Krishnan, “ Electric Motor Drives, Modeling, Analysis and Control’, Pearson Education, First Impression 2007

ET7104

DESIGN OF EMBEDDED SYSTEMS

**L T P C
3 0 0 3**

OBJECTIVES

- To provide a clear understanding on the basic concepts, Building Blocks for Embedded System
- To teach the fundamentals of System design with Partitioning
- To introduce on Embedded Process development Environment
- To study on Basic tool features for target configuration
- To introduce different EDLC Phases & Testing of embedded system

UNIT I EMBEDDED DESIGN WITH MICROCONTROLLERS 9

Product specification – Hardware / Software partitioning – Detailed hardware and software design – Integration – Product testing – Microprocessor Vs Micro Controller – Performance tools – Bench marking – RTOS Micro Controller - issues in selection of processors.

UNIT II PARTITIONING DECISION 9

Hardware / Software duality – Hardware-Software portioning- coding for Hardware-software development – ASIC revolution – Managing the Risk – Co-verification – execution environment – memory organization –memory enhancement – Firmware-speed and code density -System startup

UNIT III FUNCTIONALITIES FOR SYSTEM DESIGN 9

Timers, Watch dog timers – RAM, Flash Memory basic toolset – Integration of Hardware & Firmware- InSystem Programming, InApplication Programming,, IDE-Target Configuration- Host based debugging – Remote debugging – ROM emulators – Logic analyser

UNIT IV IN CIRCUIT EMULATORS

9

Buller proof run control – Real time trace – Hardware break points – Overlay memory – Timing constraints – Usage issues – Triggers.

UNIT V EMBEDDED DESIGN LIFE CYCLE & TESTING

9

Objective, Need, different Phases & Modelling of the EDLC.choice of Target Architectures for Embedded Application Development-for Control Dominated-Data Dominated Systems- Software &Hardware Design,PCB Design, Manufacturing & PCB Assembly-Bug tracking – reduction of risks & costs – Performance – Unit testing – Regression testing – Choosing test cases – Functional tests – Coverage tests – Testing embedded software – Performance testing – Maintenance.

TOTAL : 45 PERIODS

REFERENCES

1. James K.Peckol, "Embedded system Design",JohnWiley&Sons,2010
2. Elicia White,"Making Embedded Systems",O'Reilly Series,SPD,2011
3. Rajkamal,"Embedded Systems",TMH,2009.
4. Lyla B Das," Embedded Systems-An Integrated Approach",Pearson2013
5. Arnold S. Berger – "Embedded System Design", CMP books, USA 2002.
6. ARKIN, R.C., Behaviour-based Robotics, The MIT Press, 1998.

OBJECTIVES

- To educate on modeling and representing systems in state variable form
- To educate on solving linear and non-linear state equations
- To illustrate the role of controllability and observability
- To educate on stability analysis of systems using Lyapunov's theory
- To educate on modal concepts and design of state and output feedback controllers and estimators

UNIT I STATE VARIABLE REPRESENTATION 9
Introduction-Concept of State-State equation for Dynamic Systems-Time invariance and linearity-No uniqueness of state model-State Diagrams-Physical System and State Assignment.

UNIT II SOLUTION OF STATE EQUATIONS 9
Existence and uniqueness of solutions to Continuous-time state equations-Solution of Nonlinear and Linear Time Varying State equations-Evaluation of matrix exponential-System modes-Role of Eigenvalues and Eigenvectors.

UNIT III CONTROLLABILITY AND OBSERVABILITY 9
Controllability and Observability-Stabilizability and Detectability-Test for Continuous time Systems- Time varying and Time invariant case-Output Controllability-Reducibility-System Realizations.

UNIT IV STABILITY 9
Introduction-Equilibrium Points-Stability in the sense of Lyapunov-BIBO Stability-Stability of LTI Systems-Equilibrium Stability of Nonlinear Continuous Time Autonomous Systems-The Direct Method of Lyapunov and the Linear Continuous-Time Autonomous Systems-Finding Lyapunov Functions for Nonlinear Continuous Time Autonomous Systems-Krasovskii and Variable-Gradient Method.

UNIT V MODAL CONTROL 9
Introduction-Controllable and Observable Companion Forms-SISO and MIMO Systems-The Effect of State Feedback on Controllability and Observability-Pole Placement by State Feedback for both SISO and MIMO Systems-Full Order and Reduced Order Observers.

TOTAL: 45 PERIODS

REFERENCES:

1. M. Gopal, "Modern Control System Theory", New Age International, 2005.
2. K. Ogatta, "Modern Control Engineering", PHI, 2002.
3. John S. Bay, "Fundamentals of Linear State Space Systems", McGraw-Hill, 1999.
4. D. Roy Choudhury, "Modern Control Systems", New Age International, 2005.
5. John J. D'Azzo, C. H. Houpis and S. N. Sheldon, "Linear Control System Analysis and Design with MATLAB", Taylor Francis, 2003.
6. Z. Bubnicki, "Modern Control Theory", Springer, 2005.

OBJECTIVES

- To give an insight to the students about the significance of CMOS technology and fabrication process.
- To teach the importance and architectural features of programmable logic devices.
- To introduce the ASIC construction and design algorithms
- To teach the basic analog VLSI design techniques.
- To study the Logic synthesis and simulation of digital system with Verilog HDL.

UNIT I CMOS DESIGN 9

Overview of digital VLSI design Methodologies- Logic design with CMOS-transmission gate circuits-Clocked CMOS-dynamic CMOS circuits, Bi-CMOS circuits- Layout diagram, Stick diagram-IC fabrications – Trends in IC technology.

UNIT II PROGRAMABLE LOGIC DEVICES 12

Programming Techniques-Anti fuse-SRAM-EPROM and EEPROM technology –Re-Programmable Devices Architecture- Function blocks, I/O blocks,Interconnects, Xilinx-XC9500,Cool Runner - XC-4000,XC5200, SPARTAN, Virtex - Altera MAX 7000-Flex 10K-Stratix.

UNIT III BASIC CONSTRUCTION, FLOOR PLANNING, PLACEMENT AND ROUTING 6

System partition – FPGA partitioning – Partitioning methods- floor planning – placement-physical design flow – global routing – detailed routing – special routing- circuit extraction – DRC.

UNIT IV ANALOG VLSI DESIGN 6

Introduction to analog VLSI- Design of CMOS 2stage-3 stage Op-Amp –High Speed and High frequency op-amps-Super MOS-Analog primitive cells-realization of neural networks.

UNIT V LOGIC SYNTHESIS AND SIMULATION 12

Overview of digital design with Verilog HDL, hierarchical modelling concepts, modules and port definitions, gate level modelling, data flow modelling, behavioural modelling, task & functions, Verilog and logic synthesis-simulation-Design examples,Ripple carry Adders, Carry Look ahead adders, Multiplier, ALU, Shift Registers, Multiplexer, Comparator, Test Bench.

TOTAL 45 PERIODS**REFERENCES**

1. M.J.S Smith, "Application Specific integrated circuits",Addition Wesley Longman Inc.1997.
2. Kamran Eshraghian,Douglas A.pucknell and Sholeh Eshraghian,"Essentials of VLSI circuits and system", Prentice Hall India,2005.
3. Wayne Wolf, " Modern VLSI design " Prentice Hall India,2006.
4. Mohamed Ismail ,Terri Fiez, "Analog VLSI Signal and information Processing", McGraw Hill International Editions,1994.
5. Samir Palnitkar, "Veri Log HDL, A Design guide to Digital and Synthesis" 2nd

Ed,Pearson,2005.

6. John P. Uyemera “Chip design for submicron VLSI cmos layout and simulation “, Cengage Learning India Edition”, 2011.

EB7202

CONTROL OF ELECTRIC DRIVES

L T P C
3 0 0 3

OBJECTIVES

- To introduce the PWM converters and their analysis
- To educate on modeling of dc motor, drives and control techniques
- To educate on dynamic modeling of Induction motor drive
- To educate on the V/f and vector control of Induction motor
- To educate on generation of firing pulses and control algorithms in embedded platforms

UNIT I POWER ELECTRONIC CONVERTERS FOR DRIVES 9

Power electronic switches-state space representation of switching converters-Fixed frequency PWM-variable frequency PWM- space vector PWM- Hysteresis current control-dynamic analysis of switching converters-PWM modulator model

UNIT II CONTROL OF DC DRIVES 9

Modelling of DC machines-block diagram/transfer function-phase control-1phase/3phase converter fed DC drives- Chopper fed DC drives-four quadrant chopper circuit-closed loop control-speed control-current control-cascade control – constant torque/power operation-comparison of chopper/converter fed drives-techniques-merits/demits

UNIT III ANALYSIS AND MODELLING OF INDUCTION MOTOR DRIVE 9

Basics of induction motor drive-classification – equivalent circuit- torque Vs slip characteristics-steady state performance- Dynamic modeling of induction motor, Three phase to two phase transformation-stator, rotor, synchronously rotating reference frame model

UNIT IV CONTROL OF INDUCTION MOTOR DRIVE 9

VSI fed induction motor drives- waveforms for 1-phase, 3-phase Non-PWM and PWM VSI fed induction motor drives -principles of V/F control- principle of vector control-direct vector control- space vector modulation- indirect vector control .

UNIT V EMBEDDED CONTROL OF DRIVES 9

Generation of firing pulses- generation of PWM pulses using embedded processors- IC control of DC drives- fixed frequency/variable frequency/current control- V/F control using PIC microcontroller- vector control using embedded processors

TOTAL : 45 PERIODS

REFERENCES

1. R.Krishnan, “Electric Motor Drives, Modeling, Analysis and Control” Prentice Hall of India, 2002.

2. Thyristor control of Electric drives, Vedam Subrahmanyam, Tata McGraw Hill, 1988
3. Ion Boldea & S.A.Nasar "ELECTRIC DRIVES", CRC Press, 2006
4. Simon Ang, Alejandro Oliva "POWER SWITCHING CONVERTERS", CRC Press, 2005
5. Buxbaum, A. Schierau, and K.Staughen, "A design of control systems for DC drives", Springer- Verlag, Berlin,1990.

EB7203 MODELING AND ANALYSIS OF AC DRIVES

**L T P C
3 0 0 3**

OBJECTIVES

- To introduce the reference frame theory
- To introduce the dynamic model of induction machine in various frames
- To introduce the dynamic model of synchronous machine in various frames
- To educate on the induction motor drives
- To educate on synchronous motor drives

UNIT I REFERENCE FRAME THEORY 9

Theory of transformation – Phase transformation and commutator transformation – Invariance of Power - Static and rotating reference frames – balanced steady-state voltage and torque equations using transformation theory.

UNIT II DYNAMIC MODELLING OF INDUCTION MACHINES 9

Induction machines – Equivalent circuit – Complete speed-torque characteristics - Voltage and torque equations in static and rotating reference frames – Analysis of steady state and dynamic operations - Dynamic performance under unbalanced/fault conditions - Computer simulation.

UNIT III DYNAMIC MODELLING OF SYNCHRONOUS MACHINES 9

Synchronous machines – Equivalent circuit – Machine reactances and time constants - Voltage and torque equations in static and rotating reference frames – Analysis of steady state and dynamic operations - Dynamic performances under unbalanced/fault conditions - Computer simulation.

UNIT IV INDUCTION MOTOR DRIVES 9

Variable voltage operation – Variable frequency operation – Constant flux operation – Torque-Slip characteristics – Constant Torque and Constant power operation – Dynamic and regenerative braking of VSI fed drives – Power factor considerations – Field oriented control – Design of closed loop operation of Induction motor drive systems.

UNIT V SYNCHRONOUS MOTOR DRIVES 9

Need for leading PF operation – Open loop VSI fed drive and its characteristics – Self control – Torque control – Torque angle control – Power factor control – Brush less excitation systems – Starting methods – Field oriented control – Design of closed loop operation of Synchronous motor drive systems.

TOTAL: 45 PERIODS

REFERENCES:

1. Paul C.Krause, OlegWasyzczuk, Scott D.Sudhoff ‘Analysis of Electric Machinery and Drive Systems’ IEEE Press, Second Edition, 2002.
2. R.Krishnan,” Electric Motor Drives, Modeling, Analysis and Control” Prentice Hall of India, 2002.
3. Bose.B.K., Power Electronics and Motor Drives - Advances and Trends, IEEE Press, 2006.
4. Murphy J.M.D.,Turnbull F.G., “Thyristor control of AC Motors”, Peragamon Press,Oxford,1988.
5. Ned Mohan, Advanced Electric Drives, Analysis, Control and Modelling using Simulink MNPERE, 2001.
6. Bin Wu, “High Power Converters and AC Drives”, IEEE Press, A John Wiley and Sons, Inc., 2006.

EB7211**ELECTRIC DRIVES LABORATORY****L T P C
0 0 3 2**

Sl. No.	Title	Requirements	Quantity
1.	Speed control of Converter fed DC motor.	Power module for DC converter for separately excited DC machine 0.5HP Speed Sensor, display meters, controller circuit, CRO/DSO	1
2.	Speed control of Chopper fed DC motor.	Power module for DC chopper for separately excited DC machine 0.5HP Speed Sensor, display meters, controller circuit, CRO/DSO	1
3.	V/f control of three-phase induction motor.	IGBT inverter power module , 3 phase induction motor0.5HP,V/f controller display meters CRO/DSO	1

4.	Micro controller based speed control of Stepper motor.	Stepper motor, PIC Microcontroller, controller circuit , Interface circuit, CRO	1
5.	Speed control of BLDC motor.	Power module, BLDC motor(0.5HP) Controller circuit, sensor circuit, display meter, CRO/DSO	1
6.	DSP based speed control of SRM motor.	SRM motor-0.5 HP, PIC DSP/TMS DSP Processor, speed sensor, Power module, Display meter, DSO	1
7.	Simulation of Four quadrant operation of three-phase induction motor.	System Simulation package	5
8.	Simulation of three-phase Synchronous Generator.		
9.	Reprogrammable Logic devices and Programming		
10.	(a) VHDL programming – Examples		
	(b) Verilog HDL programming – Examples		
	(c) Realisation of control logic for electric motors using FPGA.		

TOTAL: 45 PERIODS

OBJECTIVES :

- To provide the requisite knowledge necessary to appreciate the dynamical equations involved in the analysis of different PED configurations.
- To analyze, design and simulate different power converters studied in the core courses on power converters, Inverters and dynamics of electrical machines.

1. Simulation of single phase half wave controlled converter fed RLE load
2. Simulation of single phase fully controlled converter fed RLE load.
3. Simulation of three phase half controlled converter fed RL load.
4. Simulation of single phase ac phase controlled fed RL load.
5. Simulation of three phase to single phase cyclo - converter fed RL load
6. Simulation of dynamics of armature plunger / relay contactor arrangement.
7. Simulation of single phase VSI fed RL/RC load.
8. Simulation of i) LC tank circuit resonance,
ii) Basic / modified series inverter
iii) Series loaded series resonant inverter
9. Simulation of single phase current source inverter fed induction heating load.
10. Simulation of multi level inverter topologies.
11. Numerical solution of ordinary differential equations.
12. Numerical solution of partial differential equations.

TOTAL : 45 PERIODS

REFERENCES

1. Ned Mohan, T.M Undeland and W.P Robbin, "Power Electronics: converters, Application and design" John Wiley and sons. Wiley India edition, 2006.
2. Rashid M.H., "Power Electronics Circuits, Devices and Applications ", Prentice Hall India, New Delhi, 1995.

EB7311	PROJECT WORK (PHASE I)	L T P C
		0 0 12 6

EB7411	PROJECT WORK (PHASE II)	L T P C
		0 0 24 12

ET7001	DIGITAL INSTRUMENTATION	L T P C
		3 0 0 3

OBJECTIVES

- To discuss to the students on the fundamentals building blocks of a digital instrument
- To teach the digital data communication techniques
- To study on bus communication standards and working principles
- To teach Graphical programming using GUI for instrument building
- The case studies to be developed/ discussed

UNIT I DATA ACQUISITION SYSTEMS 9

Overview of A/D converter, types and characteristics – Sampling , Errors. Objective – Building blocks of Automation systems –Counters – Modes of operation- Frequency, Period, Time interval measurements, Prescaler, Heterodyne converter for frequency measurement, Single and Multi channel Data Acquisition systems.

UNIT II INTERFACING AND DATA TRANSMISSION 9

Data transmission systems – 8086 Microprocessor based system design – Peripheral Interfaces – Time Division Multiplexing (TDM) – Digital Modulation – Pulse Modulation – Pulse Code Format – Interface systems and standards – Communications.

UNIT III INSTRUMENTATION BUS 9

Introduction, Modem standards, Basic requirements of Instrument Bus standards, Bus communication, interrupt and data handshaking , Interoperability, interchangeability for RS-232, USB, RS-422, RS-485.

UNIT IV VIRTUAL INSTRUMENTATION 9

Block diagram and Architecture – Data flow techniques – Graphical programming using GUI – Real time Embedded system –Intelligent controller – Software and hardware simulation of I/O communication blocks-peripheral interface – ADC/DAC – Digital I/O – Counter , Timer.

UN IT V CASE STUDIES 9

PC based DAS, Data loggers, PC based industrial process measurements like flow, temperature, pressure and level development system, CRT interface and controller with monochrome and colour video display.

TOTAL : 45 PERIODS

REFERENCES:

1. A.J. Bouwens, "Digital Instrumentation" , TATA McGraw-Hill Edition, 1998.

2. N. Mathivanan, "Microprocessors, PC Hardware and Interfacing", Prentice-Hall India, 2005.
3. H S Kalsi, "Electronic Instrumentation" Second Edition, Tata McGraw-Hill,2006.
4. Joseph J. Carr, "Elements of Electronic Instrumentation and Measurement" Third Edition, Pearson Education, 2003.
5. Buchanan, "Computer busses", Arnold, London,2000.
6. Jonathan W Valvano, "Embedded Microcomputer systems", Asia Pvt. Ltd., Brooks/Cole, Thomson, 2001.

ET7102

MICROCONTROLLER BASED SYSTEM DESIGN

LT P C

3 0 0 3

OBJECTIVES

- To expose the students to the fundamentals of microcontroller based system design.
- To teach I/O and RTOS role on microcontroller.
- To impart knowledge on PIC Microcontroller based system design.
- To introduce Microchip PIC 8 bit peripheral system Design
- To give case study experiences for microcontroller based applications.

UNIT I 8051 ARCHITECTURE

9

Architecture – memory organization – addressing modes – instruction set – Timers - Interrupts - I/O ports, Interfacing I/O Devices – Serial Communication.

UNIT II 8051 PROGRAMMING

9

Assembly language programming – Arithmetic Instructions – Logical Instructions –Single bit Instructions – Timer Counter Programming – Serial Communication Programming Interrupt Programming – RTOS for 8051 – RTOSLite – FullRTOS – Task creation and run – LCD digital clock/thermometer using FullRTOS

UNIT III PIC MICROCONTROLLER

9

Architecture – memory organization – addressing modes – instruction set – PIC programming in Assembly & C –I/O port, Data Conversion, RAM & ROM Allocation, Timer programming, MP-LAB.

UNIT IV PERIPHERAL OF PIC MICROCONTROLLER

9

Timers – Interrupts, I/O ports- I2C bus-A/D converter-UART- CCP modules -ADC, DAC and Sensor Interfacing –Flash and EEPROM memories.

UNIT V SYSTEM DESIGN – CASE STUDY

9

Interfacing LCD Display – Keypad Interfacing - Generation of Gate signals for converters and Inverters - Motor Control – Controlling DC/ AC appliances – Measurement of frequency - Stand alone Data Acquisition System.

TOTAL : 45 PERIODS

REFERENCES:

1. Muhammad Ali Mazidi, Rolin D. Mckinlay, Danny Causey ' PIC Microcontroller and Embedded Systems using Assembly and C for PIC18', Pearson Education 2008

2. John Iovine, 'PIC Microcontroller Project Book', McGraw Hill 2000.
3. Myke Predko, "Programming and customizing the 8051 microcontroller", Tata McGraw Hill 2001.
4. Muhammad Ali Mazidi, Janice G. Mazidi and Rolin D. McKinlay, 'The 8051 Microcontroller and Embedded Systems' Prentice Hall, 2005.
5. Rajkamal, ".Microcontrollers-Architecture, Programming, Interfacing & System Design", 2ed, Pearson, 2012.
6. I Scott Mackenzie and Raphael C.W. Phan, "The Micro controller", Pearson, Fourth edition 2012

ET7103

REAL TIME SYSTEMS

**L T P C
3 0 0 3**

OBJECTIVES

- To expose the students to the fundamentals of Real Time systems
- To teach the fundamentals of Scheduling and features of programming languages
- To study the data management system for real time
- To introduce the fundamentals of real time communication
- To teach the different algorithms and techniques used for real time systems

UNIT I INTRODUCTION

9

Introduction – Issues in Real Time Computing – Structure of a Real Time System – Task classes – Performance Measures for Real Time Systems – Estimating Program Run Times – Task Assignment and Scheduling – Classical uniprocessor scheduling algorithms – Uniprocessor scheduling of IRIS tasks – Task assignment – Mode changes and Fault Tolerant Scheduling.

UNIT II PROGRAMMING LANGUAGES AND TOOLS

9

Programming Languages and Tools – Desired language characteristics – Data typing – Control structures – Facilitating Hierarchical Decomposition, Packages, Run time (Exception) Error handling – Overloading and Generics – Multitasking – Low level programming – Task Scheduling – Timing Specifications – Programming Environments – Run – time support.

UNIT III REAL TIME DATABASES

9

Real time Databases – Basic Definition, Real time Vs General Purpose Databases, Main Memory Databases, Transaction priorities, Transaction Aborts, Concurrency control issues, Disk Scheduling Algorithms, Two – phase Approach to improve Predictability – Maintaining Serialization Consistency – Databases for Hard Real Time Systems.

UNIT IV COMMUNICATION

9

Real – Time Communication – Communications media, Network Topologies Protocols, Fault Tolerant Routing. Fault Tolerance Techniques – Fault Types – Fault Detection. Fault Error containment Redundancy – Data Diversity – Reversal Checks – Integrated Failure handling.

UNIT V EVALUATION TECHNIQUES

9

Reliability Evaluation Techniques – Obtaining parameter values, Reliability models for Hardware Redundancy – Software error models. Clock Synchronization – Clock, A Nonfault – Tolerant Synchronization Algorithm – Impact of faults – Fault Tolerant Synchronization in Hardware – Fault Tolerant Synchronization in software.

TOTAL : 45 PERIODS

REFERENCES

1. C.M. Krishna, Kang G. Shin, “Real – Time Systems”, McGraw – Hill International Editions, 1997.
2. Rajib Mall, ”Real-time systems: theory and practice”, Pearson Education, 2007
3. Peter D.Lawrence, “Real Time Micro Computer System Design – An Introduction”, McGraw Hill, 1988.
4. Stuart Bennett, “Real Time Computer Control – An Introduction”, Prentice Hall of India, 1998.
5. S.T. Allworth and R.N.Zobel, “Introduction to real time software design”, Macmillan, 2nd Edition, 1987.
6. R.J.A Buhur, D.L Bailey, “An Introduction to Real – Time Systems”, Prentice – Hall International, 1999.
7. Philip.A.Laplante, “Real Time System Design and Analysis”, Prentice Hall of India, 3rd Edition, April 2004

ET7004

PROGRAMMING WITH VHDL

**L T P C
3 0 0 3**

OBJECTIVES

- To give an insight to the students about the significance of VHDL Programming
- To teach the importance and architectural modelling of programmable logic devices.
- To introduce the construction and design programming
- To teach the basic VLSI design configurations
- To study the Logic synthesis and simulation of digital system with PLD.

UNIT I VHDL FUNDAMENTALS

9

Fundamental concepts- Modeling digital system-Domain and levels of modeling-modeling languages-VHDL modeling concepts-Scalar Data types and operations-constants and Variable-Scalar Types- Type Classification-Attributes and scalar types-expression and operators-Sequential statements.

UNIT II DATA TYPES AND BASIC MODELING CONSTRUCTS

9

Arrays- unconstrained array types-array operations and referencing- records - Access Types- Abstract Date types- -basic modeling constructs-entity declarations-Architecture

bodies-behavioral description-structural descriptions- design Processing, case study: A pipelined Multiplier accumulator.

UNIT III SUBPROGRAMS , PACKAGES AND FILES 9

Procedures-Procedure parameters- Concurrent procedure call statements –Functions – Overloading –visibility of Declarations-packages and use clauses- Package declarations- package bodies-use clauses-Predefined aliases-Aliases for Data objects-Aliases for Non-Data items-Files- I/O-Files. Case study: A bit vector arithmetic Package.

UNIT IV SIGNALS, COMPONENTS, CONFIGURATIONS 9

Basic Resolved Signals-IEEE std_Logic_1164 resolved subtypes- resolved Signal Parameters - Generic Constants- Parameterizing behavior- Parameterizing structure-components and configurations-Generate Statements-Generating Iterative structure-Conditionally generating structure-Configuration of generate statements-case study: DLX computer Systems.

UNIT V DESIGN WITH PROGRAMMABLE LOGIC DEVICES 9

Realization of -Micro controller CPU.- Memories- I/O devices-MAC- Design, synthesis, simulation and testing.

TOTAL : 45 PERIODS

REFERENCES

1. Peter J.Ashenden, “The Designer’s guide to VHDL”, Morgan Kaufmann publishers,San Francisco,Second Edition, May 2001.
2. Zainalabedin navabi, “VHDL Analysis and modeling of Digital Systems”, McGraw Hill international Editions, Second Editions, 1998.
3. Charles H Roth, Jr. “Digital system Design using VHDL”, Thomson ,2006.
4. Douglas Perry, “VHDL Programming by Example”, Tata McGraw Hill,4th Edition 2002.
5. Navabi.Z., “VHDL Analysis and Modeling of Digital Systems”, McGraw International, 1998.
6. Peter J Ashendem, “The Designers Guide to VHDL”, Harcourt India Pvt Ltd, 2002
7. Skahill. K, “VHDL for Programmable Logic”, Pearson education, 1996.

PX7204

POWER QUALITY

**LT P C
3 0 0 3**

OBJECTIVES :

- To understand the various power quality issues.
- To understand the concept of power and power factor in single phase and three phase systems supplying non linear loads
- To understand the conventional compensation techniques used for power factor correction and load voltage regulation.
- To understand the active compensation techniques used for power factor correction.
- To understand the active compensation techniques used for load voltage regulation.

UNIT I INTRODUCTION**9**

Introduction – Characterisation of Electric Power Quality: Transients, short duration and long duration voltage variations, Voltage imbalance, waveform distortion, Voltage fluctuations, Power frequency variation, Power acceptability curves – power quality problems: poor load power factor, Non linear and unbalanced loads, DC offset in loads, Notching in load voltage, Disturbance in supply voltage – Power quality standards.

UNIT II ANALYSIS OF SINGLE PHASE AND THREE PHASE SYSTEM**9**

Single phase linear and non linear loads – single phase sinusoidal, non sinusoidal source – supplying linear and nonlinear load – three phase Balance system – three phase unbalanced system – three phase unbalanced and distorted source supplying non linear loads – concept of pf – three phase three wire – three phase four wire system.

UNIT III CONVENTIONAL LOAD COMPENSATION METHODS**9**

Principle of load compensation and voltage regulation – classical load balancing problem open loop balancing – closed loop balancing, current balancing – harmonic reduction and voltage sag reduction – analysis of unbalance – instantaneous of real and reactive powers – Extraction of fundamental sequence component from measured.

UNIT IV LOAD COMPENSATION USING DSTATCOM**9**

Compensating single – phase loads – Ideal three phase shunt compensator structure – generating reference currents using instantaneous PQ theory – Instantaneous symmetrical components theory – Generating reference currents when the source is unbalanced – Realization and control of DSTATCOM – DSTATCOM in Voltage control mode

UNIT V SERIES COMPENSATION OF POWER DISTRIBUTION SYSTEM**9**

Rectifier supported DVR – Dc Capacitor supported DVR – DVR Structure – voltage Restoration – Series Active Filter – Unified power quality conditioner.

TOTAL : 45 PERIODS**REFERENCES**

1. Arindam Ghosh “Power Quality Enhancement Using Custom Power Devices”, Kluwer Academic Publishers, 2002
2. G.T.Heydt, “Electric Power Quality”, Stars in a Circle Publications, 1994(2nd edition)
3. Power Quality - R.C. Duggan
4. Power system harmonics –A.J. Arrillaga
5. Power Electronic Converter Harmonics –Derek A. Paice.

ET7006**ADVANCED DIGITAL SIGNAL PROCESSING****L T P C
3 0 0 3****OBJECTIVES**

- To expose the students to the fundamentals of digital signal processing in frequency domain & its application

- To teach the fundamentals of digital signal processing in time-frequency domain & its application
- To compare Architectures & features of Programmable DSP processors
- To discuss on Application development with commercial family of DSP Processors
- To design & develop logical functions of DSP Processors with Re-Programmable logics & Devices

UNIT I INTRODUCTION TO DIGITAL SIGNAL PROCESSING 12
 Introduction, A Digital Signal-Processing System, The Sampling Process, Discrete Time Sequences, Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear Time-Invariant Systems, Decimation and Interpolation, Digital Filters, FIR Filters, IIR Filters.

UNIT II WAVELET TRANSFORM 6
 introduction to continuous wavelet transform- discrete wavelet transform -orthogonal wavelet decomposition- Multiresolution Analysis-Wavelet function-DWT,bases,orthogonal Basis-Scaling function, Wavelet coefficients- ortho normal wavelets and their relationship to filter banks- Digital filtering interpolation (i) Decomposition filters, (ii) reconstruction, the signal- Example MRA- Haar & Daubechies wavelet.

UNIT III ARCHITECTURES OF COMMERCIAL DIGITAL SIGNAL PROCESSORS 12
 Introduction, categorisation of DSP Processors, Fixed Point (Blackfin), Floating Point (SHARC), TI TMS 320C6xxx & OMAP processors TMS320C54X & 54xx on Basic Architecture – comparison : of functional variations of Computational building blocks, MAC, Bus Architecture and memory, Data Addressing, Parallelism and pipelining, Parallel I/O interface, Memory Interface, Interrupt, DMA (one example Architecture in each of these case studies).

UNIT IV INTERFACING I/O PERIPHERALS FOR DSP BASED APPLICATIONS 6
 Introduction, External Bus Interfacing Signals, Memory Interface, Parallel I/O Interface, Programmed I/O, Interrupts and I / O Direct Memory Access (DMA).-Introduction, Design of Decimation and Interpolation Filter, FFT Algorithm, PID Controller ,Application for Serial Interfacing, DSP based Power Meter, Position control , CODEC Interface .

UNIT V VLSI IMPLEMENTATION 9
 Low power Design-need for Low power VLSI chips-Basics of DSP system architecture design using VHDL programming, Mapping of DSP algorithm onto hardware, Realisation of MAC & Filter structure.

TOTAL : 45 PERIODS

REFERENCE BOOKS:

1. John G. Proakis, Dimitris G. Manolakis, "Digital Signal Processing", Pearson Education 2002.
2. Avatar Sing, S. Srinivasan, "Digital Signal Processing- Implementation using DSP Microprocessors with Examples from TMS320C54xx", Thomson India, 2004.
3. Lars Wanhammer, "DSP Integrated Circuits", Academic press, 1999, New York.
4. Lyla B Das, "Embedded Systems-An Integrated Approach", Pearson 2013
5. Ashok Ambardar, "Digital Signal Processing: A Modern Introduction", Thomson

- India edition, 2007.
6. Raghuveer M.Rao and Ajit S. Bapardikar, Wavelet transforms- Introduction to theory and applications, Pearson Education, 2000.
 7. K.P. Soman and K.L. Ramchandran, Insight into WAVELETS from theory to practice, Eastern Economy Edition, 2008
 8. Ifeachor E. C., Jervis B. W., "Digital Signal Processing: A practical approach, Pearson-Education, PHI/ 2002
 9. B Venkataramani and M Bhaskar "Digital Signal Processors", TMH, 2nd, 2010
 10. Peter Pirsch "Architectures for Digital Signal Processing", John Wiley, 2007
 11. Vinay K.Ingle, John G.Proakis, "DSP-A Matlab Based Approach", Cengage Learning, 2010
 12. Taan S.Elali, "Discrete Systems and Digital Signal Processing with Matlab", CRC Press 2009.

ET 7003 DESIGN OF EMBEDDED CONTROL SYSTEMS

**L T P C
3 0 0 3**

OBJECTIVES

- To expose the students to the fundamentals of Embedded System Blocks
- To teach the fundamental RTOS.
- To study on interfacing for processor communication
- To compare types and Functionalities in commercial software tools
- To discuss the Applications development using interfacing

UNIT I EMBEDDED SYSTEM ORGANIZATION 9

Embedded computing – characteristics of embedded computing applications – embedded system design challenges; Build process of Realtime Embedded system – Selection of processor; Memory; I/O devices-Rs-485, MODEM, Bus Communication system using I²C, CAN, USB buses, 8 bit –ISA, EISA bus;

UNIT II REAL-TIME OPERATING SYSTEM 9

Introduction to RTOS; RTOS- Inter Process communication, Interrupt driven Input and Output -Nonmaskable interrupt, Software interrupt; Thread – Single, Multithread concept; Multitasking Semaphores.

UNIT III INTERFACE WITH COMMUNICATION PROTOCOL 9

Design methodologies and tools – design flows – designing hardware and software Interface . – system integration; SPI, High speed data acquisition and interface-SPI read/write protocol, RTC interfacing and programming;

UNIT IV DESIGN OF SOFTWARE FOR EMBEDDED CONTROL 9

Software abstraction using Mealy-Moore FSM controller, Layered software development, Basic concepts of developing device driver – SCI – Software - interfacing & porting using standard C & C++ ; Functional and performance Debugging with benchmarking Real-time system software – Survey on basics of contemporary RTOS – VXWorks, UC/OS-II

UNIT V CASE STUDIES WITH EMBEDDED CONTROLLER

9

Programmable interface with A/D & D/A interface; Digital voltmeter, control- Robot system; - PWM motor speed controller, serial communication interface.

TOTAL : 45 PERIODS

REFERENCES:

1. Steven F. Barrett, Daniel J. Pack, "Embedded Systems – Design and Applications with the 68HC 12 and HCS12", Pearson Education, 2008.
2. Raj Kamal, "Embedded Systems- Architecture, Programming and Design" Tata McGraw Hill, 2006.
3. Micheal Khevi, "The M68HC11 Microcontroller application in control,Instrumentation & Communication", PH NewJersy, 1997.
4. Chattopadhyay, "Embedded System Design",PHI Learning, 2011.
5. Muhammad Ali Mazidi, Rolin D. Mckinlay, Danny Causey, "PIC Microcontroller and Embedded Systems- Using Assembly and C for PIC18", Pearson Education,2008.
6. Steven F.Barrett,Daniel J.Pack,"Embedded Systems-Design & Application with the 68HC12 & HCS12", Pearson Education,2008.
7. Daniel W. Lewis, "Fundamentals of Embedded Software", Prentice Hall India, 2004.
8. Jack R Smith "Programming the PIC microcontroller with MBasic" Elsevier, 2007.
Keneth J.Ayala, "The 8086 Microprocessor: Programming & Interfacing the PC", Thomson India edition,

ET7204

SOFTWARE FOR EMBEDDED SYSTEMS

L T P C
3 0 0 3

OBJECTIVES

- To expose the students to the fundamentals of embedded Programming.
- To Introduce the GNU C Programming Tool Chain in Linux.
- To study the basic concepts of embedded C and Embedded OS
- To introduce time driven architecture, Serial Interface with a case study.
- To introduce the concept of embedded Java for Web Enabling of systems.

UNIT I EMBEDDED PROGRAMMING

9

C and Assembly - Programming Style - Declarations and Expressions - Arrays, Qualifiers and Reading Numbers - Decision and Control Statements - Programming Process - More Control Statements - Variable Scope and Functions - C Preprocessor - Advanced Types - Simple Pointers - Debugging and Optimization – In-line Assembly.

UNIT II C PROGRAMMING TOOLCHAIN IN LINUX

9

C preprocessor - Stages of Compilation - Introduction to GCC - Debugging with GDB - The Make utility - GNU Configure and Build System - GNU Binary utilities - Profiling - using *gprof* - Memory Leak Detection with *valgrind* - Introduction to GNU C Library

UNIT III EMBEDDED C AND EMBEDDED OS 9

Adding Structure to 'C' Code: Object oriented programming with C, Header files for Project and Port, Examples. Meeting Real-time constraints: Creating hardware delays - Need for timeout mechanism - Creating loop timeouts - Creating hardware timeouts. Creating embedded operating system: Basis of a simple embedded OS, Introduction to sEOS, Using Timer 0 and Timer 1, Portability issue, Alternative system architecture, Important design considerations when using sEOS.

UNIT IV TIME-DRIVEN MULTI-STATE ARCHITECTURE AND HARDWARE 9

Multi-State systems and function sequences: Implementing multi-state (Timed) system - Implementing a Multi-state (Input/Timed) system. Using the Serial Interface: RS232 - The Basic RS-232 Protocol - Asynchronous data transmission and baud rates - Flow control - Software architecture - Using on-chip UART for RS-232 communication - Memory requirements - The serial menu architecture - Examples. Case study: Intruder alarm system.

UNIT V EMBEDDED JAVA 9

Introduction to Embedded Java and J2ME – Smart Card basics – Java card technology overview – Java card objects – Java card applets – working with APDUs – Web Technology for Embedded Systems.

TOTAL : 45 PERIODS

REFERENCES

1. Steve Oualline, 'Practical C Programming 3rd Edition', O'Reilly Media, Inc, 2006.
2. Stephen Kochan, "Programming in C", 3rd Edition, Sams Publishing, 2009.
3. Michael J Pont, "Embedded C", Pearson Education, 2007.
4. Zhiqun Chen, 'Java Card Technology for Smart Cards: Architecture and Programmer's Guide', Addison-Wesley Professional, 2000.

PX7203

SPECIAL ELECTRICAL MACHINES

**L T P C
3 0 0 3**

OBJECTIVES

- To review the fundamental concepts of permanent magnets and the operation of permanent magnet brushless DC motors.
- To introduce the concepts of permanent magnet brushless synchronous motors and synchronous reluctance motors.
- To develop the control methods and operating principles of switched reluctance motors.
- To introduce the concepts of stepper motors and its applications.
- To understand the basic concepts of other special machines.

UNIT I	PERMANENT MAGNET BRUSHLESS DC MOTORS	9
Fundamentals of Permanent Magnets- Types- Principle of operation- Magnetic circuit analysis-EMF and Torque equations- Characteristics and control		
UNIT II	PERMANENT MAGNET SYNCHRONOUS MOTORS	9
Principle of operation – EMF and Torque equations - Phasor diagram - Power controllers – Torque speed characteristics – Digital controllers – Constructional features, operating principle and characteristics of synchronous reluctance motor.		
UNIT III	SWITCHED RELUCTANCE MOTORS	9
Constructional features –Principle of operation- Torque prediction–CharacteristicsPower controllers – Control of SRM drive- Sensorless operation of SRM – Applications.		
UNIT IV	STEPPER MOTORS	9
Constructional features –Principle of operation –Types – Torque predictions – Linear and Non-linear analysis – Characteristics – Drive circuits – Closed loop control – Applications.		
UNIT V	OTHER SPECIAL MACHINES	9
Principle of operation and characteristics of Hysteresis motor – AC series motors – Linear motor – Applications.		

TOTAL: 45 PERIODS

REFERENCES:

1. T.J.E. Miller, 'Brushless magnet and Reluctance motor drives', Clarendon press, London, 1989.
2. R.Krishnan, ' Switched Reluctance motor drives' , CRC press, 2001.
3. T.Kenjo, ' Stepping motors and their microprocessor controls', Oxford University press, New Delhi, 2000.
4. T.Kenjo and S.Nagamori, 'Permanent magnet and Brushless DC motors', Clarendon press, London, 1988.
5. R.Krishnan, ' Electric motor drives' , Prentice hall of India,2002.
6. D.P.Kothari and I.J.Nagrath, ' Electric machines', Tata Mc Graw hill publishing company, New Delhi, Third Edition, 2004.
7. Irving L.Kosow, "Electric Machinery and Transformers" Pearson Education, Second Edition, 2007.

OBJECTIVES

- To educate on formulation of optimal control problems and introduce the minimum principle
- To educate on Linear Quadratic tracking problems- in continuous and discrete domain
- To introduce the numerical techniques used for solving optimal control problems
- To educate on the concepts of filtering in the presence of noise
- To educate on the theory and design of Kalman filter

UNIT I INTRODUCTION**9**

Statement of optimal control problem – Problem formulation and forms of optimal Control – Selection of performance measures. Necessary conditions for optimal control – Pontryagin's minimum principle – State inequality constraints – Minimum time problem.

UNIT II LINEAR QUADRATIC TRACKING PROBLEMS**9**

Linear tracking problem – LQG problem – Computational procedure for solving optimal control problems – Characteristics of dynamic programming solution – Dynamic programming application to discrete and continuous systems – Hamilton Jacobi Bellman equation.

UNIT III NUMERICAL TECHNIQUES FOR OPTIMAL CONTROL**9**

Numerical solution of 2-point boundary value problem by steepest descent and Fletcher Powell method solution of Ricatti equation by negative exponential and interactive Methods

UNIT IV FILTERING AND ESTIMATION**9**

Filtering – Linear system and estimation – System noise smoothing and prediction – Gauss Markov discrete time model – Estimation criteria – Minimum variance estimation – Least square estimation – Recursive estimation.

UNIT V KALMAN FILTER AND PROPERTIES**9**

Filter problem and properties – Linear estimator property of Kalman Filter – Time invariance and asymptotic stability of filters – Time filtered estimates and signal to noise ratio improvement – Extended Kalman filter.

TOTAL : 45 PERIODS**REFERENCES:**

1. KiRk D.E., 'Optimal Control Theory – An introduction', Prentice hall, N.J., 1970.
2. Sage, A.P., 'Optimum System Control', Prentice Hall N.H., 1968.
3. Anderson, B.D.O. and Moore J.B., 'Optimal Filtering', Prentice hall Inc., N.J., 1979.
4. S.M. Bozic, "Digital and Kalman Filtering", Edward Arnould, London, 1979.
5. Astrom, K.J., "Introduction to Stochastic Control Theory", Academic Press, Inc, N.Y., 1970.
6. Alok Sinha, Linear Systems Optimal and Robust Control, CRC Press, First Indian Reprint,2009.

PRE-REQUISITES: Basic Instrumentation ,Material Science,Programming

OBJECTIVES

- To teach the students properties of materials ,microstructure and fabrication methods.
- To teach the design and modeling of Electrostatic sensors and actuators.
- To teach the characterizing thermal sensors and actuators through design and modeling
- To teach the fundamentals of piezoelectric sensors and actuators
- To give exposure to different MEMS and NEMS devices.

UNIT I MEMS:MICRO-FABRICATION, MATERIALS AND ELECTRO-MECHANICAL CONEPTS 9

Overview of micro fabrication – Silicon and other material based fabrication processes – Concepts: Conductivity of semiconductors-Crystal planes and orientation-stress and strain-flexural beam bending analysis-torsional deflections-Intrinsic stress- resonant frequency and quality factor.

UNIT II ELECTROSTATIC SENSORS AND ACTUATION 9

Principle, material, design and fabrication of parallel plate capacitors as electrostatic sensors and actuators-Applications

UNIT III THERMAL SENSING AND ACTUATION 9

Principle, material, design and fabrication of thermal couples, thermal bimorph sensors, thermal resistor sensors-Applications.

UNIT IV PIEZOELECTRIC SENSING AND ACTUATION 9

Piezoelectric effect-cantilever piezo electric actuator model-properties of piezoelectric materials-Applications.

UNIT V CASE STUDIES 9

Piezoresistive sensors, Magnetic actuation, Micro fluidics applications, Medical applications, Optical MEMS.-NEMS Devices

TOTAL : 45 PERIODS

REFERENCES

1. Chang Liu, “Foundations of MEMS”, Pearson International Edition, 2006.
2. Marc Madou , “Fundamentals of microfabrication”,CRC Press, 1997.
3. Boston , “Micromachined Transducers Sourcebook”,WCB McGraw Hill, 1998.
4. M.H.Bao “Micromechanical transducers :Pressure sensors, accelerometers and gyroscopes”, Elsevier, Newyork, 2000.
5. P. RaiChoudry“ MEMS and MOEMS Technology and Applications”, PHI, 2012.
6. Stephen D. Senturia, “ Microsystem Design”, Springer International Edition, 2011.

OBJECTIVES :

- To Provide knowledge about the stand alone and grid connected renewable energy systems.
- To equip with required skills to derive the criteria for the design of power converters for renewable energy applications.
- To analyse and comprehend the various operating modes of wind electrical generators and solar energy systems.
- To design different power converters namely AC to DC, DC to DC and AC to AC converters for renewable energy systems.
- To develop maximum power point tracking algorithms.

UNIT I INTRODUCTION 9

Environmental aspects of electric energy conversion: impacts of renewable energy generation on environment (cost-GHG Emission) - Qualitative study of different renewable energy resources ocean, Biomass, Hydrogen energy systems : operating principles and characteristics of: Solar PV, Fuel cells, wind electrical systems-control strategy, operating area.

UNIT II ELECTRICAL MACHINES FOR RENEWABLE ENERGY CONVERSION 9

Review of reference theory fundamentals-principle of operation and analysis: IG, PMSG, SCIG and DFIG.

UNIT III POWER CONVERTERS 9

Solar: Block diagram of solar photo voltaic system : line commutated converters (inversion-mode) - Boost and buck-boost converters- selection Of inverter, battery sizing, array sizing.

Wind: three phase AC voltage controllers- AC-DC-AC converters: uncontrolled rectifiers, PWM Inverters, Grid Interactive Inverters-matrix converters.

UNIT IV ANALYSIS OF WIND AND PV SYSTEMS 9

Stand alone operation of fixed and variable speed wind energy conversion systems and solar system-Grid connection Issues -Grid integrated PMSG and SCIG Based WECS- Grid Integrated solar system

UNIT V HYBRID RENEWABLE ENERGY SYSTEMS 9

Need for Hybrid Systems- Range and type of Hybrid systems- Case studies of Wind-PV- Maximum Power Point Tracking (MPPT).

TOTAL : 45 PERIODS**REFERENCES:**

1. S.N.Bhadra, D. Kastha, & S. Banerjee "Wind Electrical Systems", Oxford University Press, 2009
2. Rashid .M. H "power electronics Hand book", Academic press, 2001.
3. Rai. G.D, "Non conventional energy sources", Khanna publishes, 1993.
4. Rai. G.D," Solar energy utilization", Khanna publishes, 1993.

5. Gray, L. Johnson, "Wind energy system", prentice hall inc, 1995.
6. Non-conventional Energy sources B.H.Khan Tata McGraw-hill Publishing Company, New Delhi.

CL7004

ROBOTICS AND CONTROL

L T P C
3 0 0 3

OBJECTIVES

- To introduce robot terminologies and robotic sensors
- To educate direct and inverse kinematic relations
- To educate on formulation of manipulator Jacobians and introduce path planning techniques
- To educate on robot dynamics
- To introduce robot control techniques

UNIT I INTRODUCTION AND TERMINOLOGIES 9

Definition-Classification-History- Robots components-Degrees of freedom-Robot joints-coordinates- Reference frames-workspace-Robot languages-actuators-sensors-Position, velocity and acceleration sensors-Torque sensors-tactile and touch sensors-proximity and range sensors- vision system-social issues

UNIT II KINEMATICS 9

Mechanism-matrix representation-homogenous transformation-DH representation-Inverse kinematics-solution and programming-degeneracy and dexterity

UNIT III DIFFERENTIAL MOTION AND PATH PLANNING 9

Jacobian-differential motion of frames-Interpretation-calculation of Jacobian-Inverse Jacobian- Robot Path planning

UNIT IV DYNAMIC MODELLING 9

Lagrangian mechanics- Two-DOF manipulator- Lagrange-Euler formulation – Newton-Euler formulation – Inverse dynamics

UNIT V ROBOT CONTROL SYSTEM 9

Linear control schemes- joint actuators- decentralized PID control- computed torque control – force control- hybrid position force control- Impedance/ Torque control

TOTAL : 45 PERIODS

REFERENCES

1. R.K. Mittal and I J Nagrath, " Robotics and Control", Tata MacGrawHill, Fourth Reprint 2003.
2. Saeed B. Niku, "Introduction to Robotics ", Pearson Education, 2002
3. Fu, Gonzalez and Lee Mcgrahill, "Robotics ", international
4. R.D. Klaffer, TA Chmielewski and Michael Negin, "Robotic Engineering, An Integrated approach", Prentice Hall of India, 2003.
5. Reza N.Jazar, Theory of Applied Robotics Kinematics, Dynamics and Control, Springer, FIST Indian Reprint 2010.

OBJECTIVES

- To introduce various model structures for system identification
- To impart knowledge on parametric and non-parametric identification
- To introduce non-linear identification techniques
- To introduce the concept of adaptation techniques and control
- To illustrate the identification and adaptive control techniques through case studies

UNIT I MODELS FOR IDENTIFICATION 9

Models of LTI systems: Linear Models-State space Models-OE model- Model sets, Structures and Identifiability-Models for Time-varying and Non-linear systems: Models with Nonlinearities – Non-linear state-space models-Black box models, Fuzzy models’.

UNIT II NON-PARAMETRIC AND PARAMETRIC IDENTIFICATION 9

Transient response and Correlation Analysis – Frequency response analysis – Spectral Analysis – Least Square – Recursive Least Square –Forgetting factor- Maximum Likelihood – Instrumental Variable methods.

UNIT III NON-LINEAR IDENTIFICATION 9

Open and closed loop identification: Approaches – Direct and indirect identification – Joint input-output identification – Non-linear system identification – Wiener models – Power series expansions - State estimation techniques – Non linear identification using Neural Network and Fuzzy Logic.

UNIT IV ADAPTIVE CONTROL AND ADAPTATION TECHNIQUES 9

Introduction – Uses – Auto tuning – Self Tuning Regulators (STR) – Model Reference Adaptive Control (MRAC) – Types of STR and MRAC – Different approaches to self-tuning regulators – Stochastic Adaptive control – Gain Scheduling.

UNIT V CASE STUDIES 9

Inverted Pendulum, Robot arm, process control application: heat exchanger, Distillation column, application to power system, Ship steering control.

TOTAL : 45 PERIODS**REFERENCES**

1. Ljung, " System Identification Theory for the User", PHI, 1987.
2. Torsten Soderstrom, Petre Stoica, "System Identification", prentice Hall ` International (UK) Ltd,1989.
3. Astrom and Wittenmark," Adaptive Control ", PHI
4. William S. Levine, " Control Hand Book". Narendra and Annasamy," Stable Adaptive Control Systems, Prentice Hall, 1989.

OBJECTIVES

- To Study about solar modules and PV system design and their applications
- To Deal with grid connected PV systems
- To Discuss about different energy storage systems

UNIT I INTRODUCTION 9

Characteristics of sunlight – semiconductors and P-N junctions –behavior of solar cells – cell properties – PV cell interconnection

UNIT II STAND ALONE PV SYSTEM 9

Solar modules – storage systems – power conditioning and regulation - protection – stand alone PV systems design – sizing

UNIT III GRID CONNECTED PV SYSTEMS 9

PV systems in buildings – design issues for central power stations – safety – Economic aspect – Efficiency and performance - International PV programs

UNIT IV ENERGY STORAGE SYSTEMS 9

Impact of intermittent generation – Battery energy storage – solar thermal energy storage – pumped hydroelectric energy storage

UNIT V APPLICATIONS 9

Water pumping – battery chargers – solar car – direct-drive applications –Space – Telecommunications.

REFERENCES:

1. Eduardo Lorenzo G. Araujo, Solar electricity engineering of photovoltaic systems, Progensa,1994.
2. Stuart R.Wenham, Martin A.Green, Muriel E. Watt and Richard Corkish, Applied Photovoltaics, 2007,Earthscan, UK.
3. Frank S. Barnes & Jonah G. Levine, Large Energy storage Systems Handbook , CRC Press, 2011.
4. Solar & Wind Energy Technologies – McNeils, Frenkel, Desai, Wiley Eastern, 1990
5. Solar Energy – S.P. Sukhatme, Tata McGraw Hill,1987.

ET7012 COMPUTER IN NETWORKING AND DIGITAL CONTROL L T P C
3 0 0 3

Pre-requisites: Digital Circuits, Computer Technology, Basic in Measurement & Instrumentation

OBJECTIVES

- To discuss on the fundamentals of Network Layers for Data Communications
- To teach the digital data communication techniques
- To teach Graphical programming using GUI for instrument building
- To study on internet based communication standards and working principles
- The case studies to be developed/ discussed in Virtual Environment Tools

UNIT I NETWORK FUNDAMENTALS 9

Data communication networking – Data transmission concepts – Communication networking - Overview of OSI- TCP/IP layers – IP addressing - DNS – Packet Switching – Routing – Fundamental concepts in SMTP, POP, FTP, Telnet, HTML, HTTP, URL, SNMP, ICMP.

UNIT II DATA COMMUNICATION 9

Sensor data acquisition, Sampling, Quantization, Filtering, Data Storage, Analysis using compression techniques, Data encoding – Data link control – Framing, Flow and Error control, Point to point protocol, Routers, Switches, Bridges – MODEMs, Network layer – Congestion control, Transport layer- Congestion control, Connection establishment.

UNIT III VIRTUAL INSTRUMENTATION 9

Block diagram and Architecture – Data flow techniques – Graphical programming using GUI – Real time system – Embedded controller – Instrument drivers – Software and hardware simulation of I/O communication blocks – ADC/DAC – Digital I/O – Counter, Timer, Data communication ports.

UNIT IV MEASUREMENT AND CONTROL THROUGH INTERNET 9

Web enabled measurement and control-data acquisition for Monitoring of plant parameters through Internet – Calibration of measuring instruments through Internet, Web based control – Tuning of controllers through Internet

UNIT V VI BASED MEASUREMENT AND CONTROL 9

Simulation of signal analysis & controller logic modules for Virtual Instrument control – Case study of systems using VI for data acquisition, Signal analysis, controller design, Drives control.

TOTAL: 45 PERIODS

REFERENCES:

1. Wayne Tomasi, "Introduction to Data communications and Networking" Pearson Education, 2007.
2. Al Williams, "Embedded Internet Design", Second Edition, TMH, 2007.
3. Douglas E.Comer, "Internetworking with TCP/IP, Vol. 1", Third Edition, Prentice Hall, 1999.
4. Cory L. Clark, "LabVIEW Digital Signal Processing and Digital Communication", TMH edition 2005.
5. Behrouza A Forouzan,"Data Communications and Networking" Fourth edition, TMH, 2007.
6. Krishna Kant,"Computer based Industrial control",PHI,2002.
7. Gary Johnson, "LabVIEW Graphical Programming", Second edition, McGraw Hill, Newyork, 1997.
8. Kevin James, "PC Interfacing and Data Acquisition: Techniques for measurement, Instrumentation and control, Newnes, 2000.
9. Cory L. Clark,"LabVIEW Digital Signal processing and Digital Communications" Tata McGRAW-HILL edition, 2005.

PS7007

WIND ENERGY CONVERSION SYSTEMS

**L T P C
3 0 0 3**

OBJECTIVES

- To learn the design and control principles of Wind turbine.
- To understand the concepts of fixed speed and variable speed, wind energy conversion systems.
- To analyze the grid integration issues.

UNIT I INTRODUCTION

9

Components of WECS-WECS schemes-Power obtained from wind-simple momentum theory-Power coefficient-Sabinin's theory-Aerodynamics of Wind turbine

UNIT II WIND TURBINES

9

HAWT-VAWT-Power developed-Thrust-Efficiency-Rotor selection-Rotor design considerations-Tip speed ratio-No. of Blades-Blade profile-Power Regulation-yaw control-Pitch angle control-stall control-Schemes for maximum power extraction.

UNIT III FIXED SPEED SYSTEMS

9

Generating Systems- Constant speed constant frequency systems -Choice of Generators-Deciding factors-Synchronous Generator-Squirrel Cage Induction Generator- Model of Wind Speed- Model wind turbine rotor - Drive Train model-Generator model for Steady state and Transient stability analysis.

UNIT IV VARIABLE SPEED SYSTEMS **9**
 Need of variable speed systems-Power-wind speed characteristics-Variable speed constant frequency systems synchronous generator- DFIG- PMSG -Variable speed generators modeling - Variable speed variable frequency schemes.

UNIT V GRID CONNECTED SYSTEMS **9**
 Wind interconnection requirements, low-voltage ride through (LVRT), ramp rate limitations, and supply of ancillary services for frequency and voltage control, current practices and industry trends wind interconnection impact on steady-state and dynamic performance of the power system including modeling issue.

TOTAL: 45 PERIODS

REFERENCES

1. L.L.Freris “Wind Energy conversion Systems”, Prentice Hall, 1990
2. S.N.Bhadra, D.Kastha,S.Banerjee,”Wind Electrical Sytems”,Oxford University Press,2010.
3. Ion Boldea, “Variable speed generators”, Taylor & Francis group, 2006.
4. E.W.Golding “The generation of Electricity by wind power”, Redwood burn Ltd., Trowbridge,1976.
5. N. Jenkins,” Wind Energy Technology” John Wiley & Sons,1997
6. S.Heir “Grid Integration of WECS”, Wiley 1998.

CL7002

ROBUST CONTROL

L T P C
3 0 0 3

OBJECTIVES

- To introduce norms, random spaces and robustness measures
- To educate on H₂ optimal control and estimation techniques
- To educate on H_∞ optimal control techniques
- To educate on the LMI approach of H_∞ control
- To educate on synthesis techniques for robust controllers and illustrate through case studies

UNIT I INTRODUCTION **9**
 Norms of vectors and Matrices – Norms of Systems – Calculation of operator Norms – vector Random spaces- Specification for feedback systems – Co-prime factorization and Inner functions –structured and unstructured uncertainty- robustness

UNIT II H₂ OPTIMAL CONTROL **9**
 Linear Quadratic Controllers – Characterization of H₂ optimal controllers – H₂ optimal estimation-Kalman Bucy Filter – LQG Controller

UNIT III H-INFINITY OPTIMAL CONTROL-RICCATI APPROACH 9

Formulation – Characterization of H-infinity sub-optimal controllers by means of Riccati equations – H-infinity control with full information – H-infinity estimation

UNIT IV H-INFINITY OPTIMAL CONTROL- LMI APPROACH 9

Formulation – Characterization of H-infinity sub-optimal controllers by means of LMI Approach – Properties of H-infinity sub-optimal controllers – H-infinity synthesis with pole-placement constraints

UNIT V SYNTHESIS OF ROBUST CONTROLLERS & CASE STUDIES 9

Synthesis of Robust Controllers – Small Gain Theorem – D-K –iteration- Control of Inverted Pendulum- Control of CSTR – Control of Aircraft – Robust Control of Second-order Plant- Robust Control of Distillation Column

TOTAL : 45 PERIODS

REFERENCES

1. U. Mackenroth “Robust Control Systems: Theory and Case Studies”, Springer International Edition, 2010.
2. J. B. Burl, “ Linear optimal control H2 and H-infinity methods”, Addison W Wesley, 1998
3. D. Xue, Y.Q. Chen, D. P. Atherton, "Linear Feedback Control Analysis and Design with MATLAB, Advances In Design and Control”, Society for Industrial and Applied Mathematics, 2007.
4. I.R. Petersen, V.A. Ugrinovskii and A. V. Savkin, “Robust Control Design using H - infinity Methods”, Springer, 2000.
5. M. J. Grimble, “Robust Industrial Control Systems: Optimal Design Approach for Polynomial Systems”, John Wiley and Sons Ltd., Publication, 2006.